

		ISSUE CLASSIFICATION
Class	Subclass	

PATENT NUMBER

U.S. UTILITY Patent Application

Q.A. A.G.

PATENT DATE

EXAMINEE

APPLICANTS

Знаменитости

Semiconductor integrated circuit and method for initializing the same

PTO-2040
12/89

[illegible]

☐ Continued on Issue Slip Inside File Jacket

☐ **TERMINAL
DISCLAIMER**

☐ The terminal _____ months of this patent have been disclaimed.

DRAWINGS

Sheets Drwg.

Figs. Drwg.

Print Fig.

(Assistant Examiner)

(Date)

(Primary Examiner)

(Date)

(Legal Instruments Examiner)

(Date)

CLAIMS ALLOWED

Total Claims

Print Claim for O.G.

NOTICE OF ALLOWANCE MAILED

ISSUE FEE

Amount Due

Date Paid

ISSUE BATCH NUMBER

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Form **PTO-436A**
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